

Join a fast-moving health focused technology company creating simple, smart and personalized wearables designed to help individuals on their health journey optimize for good health today and to help prevent and manage chronic diseases in the future. We are developing a proprietary platform that uses RF technology, which we believe will enable the creation of low-cost and scalable sensors that are small enough to fit into a wearable and other small form factors.

We require talented RFIC engineers with a whole system view to work on innovative RF SoCs in highly advanced process nodes.

The ideal applicant will have an MSEE with 5+ years or PhD and 3+ years experience

Qualifications/requirements:

- Experience of working in the Cadence Virtuoso mixed signal design environment
- Experience with RF CMOS design and layout
- Experience of EM simulation tools such as Cadence EMX, Ansys HFSS or Keysight ADS
- Experience of Keysight ADS RF circuit design and simulation
- Circuit experience of LNA, Mixer, PLL, PA, Baseband Filter design
- mmWave experience desirable
- Understanding of RFIC/Analog DFM and DFT
- Experience of digitally assisted analog and analog calibration loops.
- Area/power optimization, and design trade-off analysis
- Experience with laboratory chip bring-up and validation support

We're looking for people with strong multi-functional collaboration capability who are self-motivated and proactive.

You will be located in our European HQ in Cork, Ireland. Partial remote working can be accommodated.