February 25, 2022

Join a fast-moving health focused technology company creating simple, smart and personalized wearables designed to help individuals on their health journey optimize for good health today and to help prevent and manage chronic diseases in the future. We are developing a proprietary platform that uses RF technology, which we believe will enable the creation of low-cost and scalable sensors that are small enough to fit into a wearable and other small form factors.

We require talented ASIC/SoC digital design engineers with a whole system view to work on innovative SoCs in highly advanced process nodes.

The ideal applicant will have an MSEE with 5+ years of ASIC design and/or verification experience.

Qualification:

- In depth understanding and knowledge of ASIC design flow.
- Understanding of wirless systems, DSP system design, filters.
- Experience in micro-architecture design, RTL coding, and functional verification.
- Good understanding of serial protocols such as SPI, I2C, UART
- Experience designing blocks for ASIC/SoCs with integrated RF
- Proficient in design and verification tools.
- Good understanding of synchronous/asynchronous design,
- Profieciency in ASIC power/area trade-off analysis and optimization.
- Experience in chip bring up and validation
- Experience with prototyping systems on FPGA platforms or emulators.
- Experience in system Verilog is preferable
- Prior experience working for startups highly desirable

Description:

- Block-level micro-architecture design, RTL coding, verification, documentation
- Area/power optimization, and design trade-off analysis
- Block and chip-level synthesis and formal verification
- Work with physical design team to achieve timing closure and sign-off on timing
- Chip bring-up and validation support

We're looking for people with strong multi-functional collaboration capability who are self-motivated and proactive.

You will be located in our European HQ in Cork, Ireland. Partial remote working can be accommodated.